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<http://www.atmel.com/devices/ATMEGA328.aspx?tab=documents>

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http://en.wikipedia.org/wiki/Intel_MCS-51

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A key design goal was achieving low-latency input/output (interrupt) M-profile, the "Microcontroller" profile, implemented by most cores in the Cortex-M series.

http://en.wikipedia.org/wiki/ARM_architecture

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